

MAY 28 2010

Application Serial No. 10/584,778
Reply to final office action of March 4, 2010

PATENT
Docket: CU-4906

Amendments to the Claims

The listing of claims presented below will replace all prior versions, and listings, of claims in the application.

Listing of claims:

1. **(currently amended)** A data write-in method for a flash memory, wherein the flash memory comprises at least two flash chips and a controller, and the method comprises:

partitioning physical blocks in the flash chips to odd logical block addresses and even logical block addresses, respectively;

the controller receiving a data write-in-instructions instruction and analyzing a beginning logical address for writing from the received data write-in-instructions Instruction;

the controller obtaining the logical block address needed to be written according to the analyzed beginning logical address;

the controller determining a parity of the logical block address[[]], and selecting one flash chip from the flash chips according to the determined parity of the logical block address;

the controller directing a first programming or erasing-instructions instruction to the physical blocks corresponding to the obtained logical block address in the selected flash chip;

the controller detecting whether the other flash chip needs to be programmed or erased during while the first programming or erasing instruction is-instructions are being processed; if need programming or erasing is needed in the other flash chip, the method further comprises:

the controller directing a second programming or erasing instruction instructions to the other flash chip of at least two flash chips.

2. (canceled)

3. **(currently amended)** The data write-in method for a flash memory according to

Application Serial No. 10/584,778
Reply to final office action of March 4, 2010

PATENT
Docket: CU-4906

claim 1, wherein if the other flash chip does not need to be programmed or erased, the method further comprises:

judging whether the processing of the first programming or erasing instructions is finished, ~~if yes, returning to the processing of receiving; otherwise, returning to the processing of obtaining.~~

4-5. (canceled)

6. (previously presented) The data write-in method for a flash memory according to claim 1, wherein the analyzing further comprises:

obtaining the number of sectors needed to be written from the data writing operation instruction.

7. (currently amended) The data write-in method for a flash memory according to claim 6, the analyzing further comprises:

judging whether the data write-in instruction has writing operation instructions have been finished by subtracting a number ~~for~~ of written sectors from a number ~~for~~ of need-to-be-written-sectors.